

ABSTRACT

In a cell assembly and disassembly device having a cell disassembly unit (2) for disassembling the cell received from an ATM circuit interface, extracting data from the payload, distributing the data in plural time slots divided and multiplexed in time in frame period, and sending out to an STM circuit interface, the cell disassembly unit (2) comprises a fluctuation absorption buffer unit (11) provided in each time slot, and the data distributed in each time slot is once held in these buffers to absorb cell fluctuations, thereby suppressing both increase of total capacity of the memory for absorbing fluctuations and complication of memory control circuit, and reducing the cost.

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